

#8
C. Barnes

T3006-906838

IN THE UNITED STATES DESIGNATED/ELECTED OFFICE (D.O./E.O./US)

Applicants: Christian GOIRE ET AL.

International
Application No.: PCT/FR00/01815

International
Filing Date: 28 June 2000

U.S. Serial No.: To be Assigned

U.S. Filing Date: March 1, 2001

For: **METHOD FOR VERIFYING CODE TRANSFORMERS
FOR AN EMBEDDED SYSTEM, IN PARTICULAR
IN A CHIP CARD**

McLean, Virginia

PROPOSED DRAWING CORRECTIONS

Hon. Commissioner of Patents and Trademarks
Washington, D.C. 20231

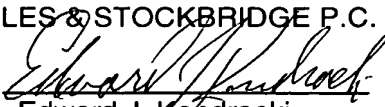
Sir:

Applicant requests approval of the drawing corrections on Fig. 1 - 3 as shown in red on the attached two (2) sheets.

The proposed corrections only comprise labeling of the blocks of the drawings to conform to the specification and claims and removing the headings "1/2" to "2/2" to conform the drawings to U.S. practice.

Respectfully submitted,

MILES & STOCKBRIDGE P.C.

By: 
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Date: March 1, 2001

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